

10Gb/s SFP+ BIDI 80km Transceiver HXSX-AL45/5483x

Features

- Up to 11.3Gb/s data links
- 1490nm EML laser and APD receiver for HXSX-AL4583x
- 1550nm EML laser and APD receiver for HXSX-AL5483x
- Up to 80km on 9/125µm SMF
- Hot-pluggable SFP+ footprint
- BIDI LC optical connector
- RoHS-10 compliant and lead-free
- Single +3.3V power supply
- Compliant with SFF+MSA and SFF-8472
- Support Digital Monitoring interface
- Metal enclosure, for lower EMI
- Meet ESD requirements, resist 8KV direct contact voltage
- Case operating temperature

Commercial: $0 \sim +70^{\circ}$ C Extended: $-10 \sim +80^{\circ}$ C Industrial: $-40 \sim +85^{\circ}$ C



Applications

- 10GBASE-ZR/ZW & 10G Ethernet
- SDH STM64
- Other Optical Links

Part Number Ordering Information

Part Number	Data Rate (Gb/s)	Wavelength (nm)	Transmission Distance(km)	Temperature (°C) (Operating Case)
HXSX-AL45/5483C	10.3125		80	0~70 commercial
HXSX-AL45/5483E	10.3125	1490Tx/1550Rx	80	0~70 commercial
HXSX-AL45/5483I	10.3125	1550Tx/1490Rx	80	-40~85 Industrial



I. Absolute Maximum Ratings

It has to be noted that the operation in excess of any individual absolute maximum ratings might cause permanent damage to this module.

Parameter	Symbol	Min	Max	Unit	Notes
Storage Temperature	T_{S}	-40	85	°C	
Power Supply Voltage	V_{CC}	-0.5	3.6	V	
Relative Humidity (non-condensation)	RH	5	95	%	
Damage Threshold	TH_d	0		dBm	

II. Recommended Operating Conditions

Parameter	Symbol	Min	Typical	Max	Unit	Notes
		0		70		commercial
Operating Case	T_{OP}	-10		80	°C	extended
Temperature		-40		85		Industrial
Power Supply Voltage	V_{CC}	3.135	3.3	3.465	V	
Data Rate			10.3125		Gb/s	
Control Input Voltage High		2		Vcc	V	
Control Input Voltage Low		0		0.8	V	
Link Distance (SMF)	D			80	km	9/125um

III. General Description

Walsun'HXSX-AL4583x/HXSX-AL5483x SFP+ transceiver is designed for use in 10-Gigabit Ethernet links up to 80km over single mode fiber. The module consists of EML Laser, APD and Preamplifier in a high-integrated optical sub-assembly. Digital monitoring functions are available via a 2-wire serial interface, as specified in SFF-8472.

HXSX-AL4583x/HXSX-AL5483x transceivers provide a unique enhanced digital monitoring interface, which allows real-time access to device operating parameters such as transceiver temperature, laser bias current, transmitted optical power, and received optical power and transceiver supply voltage. It also defines a sophisticated system of alarm and warning flags,



which alerts end-users when particular operating parameters are outside of a factory set normal range.

The SFP+ MSA defines a 256-byte memory map in EEPROM that is accessible over a 2-wire serial interface at the 8bit address 1010000X (A0h). The digital diagnostic monitoring interface makes use of the 8bit address 1010001X (A2h), so the originally defined serial ID memory map remains unchanged.

IV. Pin Assignment and Pin Description

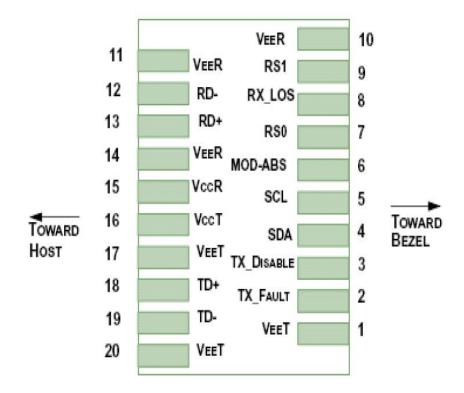


Figure 1. Diagram of host board connector block pin numbers and names

Pin	Symbol	Name/Description	Notes
1	${ m V}_{_{ m EET}}$	Transmitter Ground (Common with Receiver Ground)	1
2	$\mathrm{T}_{_{\mathrm{FAULT}}}$	Transmitter Fault.	2
3	T _{dis}	Transmitter Disable. Laser output disabled on high or open.	3
4	SDA	2-wire Serial Interface Data Line	4
5	SCL	2-wire Serial Interface Clock Line	4



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6	MOD_ABS	Module Absent. Grounded within the module	4
7	RS0	Rate Select 0	5
8	LOS	Loss of Signal indication. Logic 0 indicates normal operation.	6
9	RS1	No connection required	
10	V _{EER}	Receiver Ground (Common with Transmitter Ground)	1
11	V _{EER}	Receiver Ground (Common with Transmitter Ground)	1
12	RD-	Receiver Inverted DATA out. AC Coupled	
13	RD+	Receiver Non-inverted DATA out. AC Coupled	
14	V _{EER}	Receiver Ground (Common with Transmitter Ground)	1
15	V _{CCR}	Receiver Power Supply	
16	V _{CCT}	Transmitter Power Supply	
17	V EET	Transmitter Ground (Common with Receiver Ground)	1
18	TD+	Transmitter Non-Inverted DATA in. AC Coupled.	
19	TD-	Transmitter Inverted DATA in. AC Coupled.	
20	V EET	Transmitter Ground (Common with Receiver Ground)	1

Notes:

- 1. Circuit ground is internally isolated from chassis ground.
- 2. TFAULT is an open collector/drain output, which should be pulled up with a $4.7k\Omega$ - $10k\Omega$ resistor on the host board if intended for use. Pull up voltage should be between 2.0V to Vcc + 0.3V. A high output indicates a transmitter fault caused by either the TX bias current or the TX output power exceeding the preset alarm thresholds. A low output indicates normal operation. In the low state, the output is pulled to <0.8V.
- 3. Laser output disabled on TDIS >2.0V or open, enabled on TDIS <0.8V.
- 4. Should be pulled up with $4.7k\Omega-10k\Omega$ on host board to a voltage between 2.0V and 3.6V. MOD_ABS pulls line low to indicate module is plugged in.
- 5. Internally pulled down per SFF-8431 Rev 4.1.
- 6. LOS is open collector output. It should be pulled up with $4.7k\Omega-10k\Omega$ on host board to a voltage between 2.0V and 3.6V. Logic 0 indicates normal operation; logic 1 indicates loss of signal.



V. Electrical Characteristics

The following electrical characteristics are defined over the Recommended Operating Environment unless otherwise specified.

Parameter	Symbol	Min.	Тур.	Max	Unit	Notes			
Power Consumption	р			1.8	W				
Supply Current	Icc			520	mA				
	Transmitter								
Single-ended Input Voltage Tolerance	Vcc	-0.3		4.0	V				
AC Common Mode Input Voltage Tolerance (RMS)		15			mV				
Differential Input Voltage Swing	Vin,pp	180		820	mVpp				
Differential Input Impedance	Zin	90	100	110	Ohm	1			
Transmit Disable Assert Time				10	us				
Transmit Disable Voltage	Vdis	Vcc-1.3		Vcc	V				
Transmit Enable Voltage	Ven	Vee		Vee +0.8	V	2			
	Re	eceiver							
Differential Output Voltage Swing	Vout,pp	350		850	mVpp				
Differential Output Impedance	Zout	90	100	110	Ohm	3			
Data output rise/fall time	Tr/Tf	28			ps	4			
LOS Assert Voltage	VlosH	Vcc-1.3		Vcc	V	5			
LOS De-assert Voltage	VlosL	Vee		Vee +0.8	V	5			
Power Supply Rejection	PSR	100			mVpp	6			

Notes:

- 1. Connected directly to TX data input pins. AC coupled thereafter.
- 2. Or open circuit.
- 3. Input 100 ohms differential termination.
- 4. These are unfiltered 20-80% values.
- 5. Loss of Signal is LVTTL. Logic 0 indicates normal operation; logic 1 indicates no signal detected.
- 6. Receiver sensitivity is compliant with power supply sinusoidal modulation of 20 Hz to 1.5 MHz up to specified value applied through the recommended power supply filtering network.



VI. Optical Characteristics

The following optical characteristics are defined over the Recommended Operating Environment unless otherwise specified.

Parameter	Symbol	Min.	Typical	Max	Unit	Notes		
Transmitter								
Center Wavelength		1480	1490	1500		4583x		
	λ	1540	1550	1560	nm	5483x		
Optical Spectral Width	Δλ			1	nm			
Side Mode Suppression Ratio	SMSR	30			dB			
Average Optical Power	P _{AVG}	0		5	dBm			
Optical Extinction Ratio	ER	8.2			dB			
Dispersion Penalty	DP			3.0	dB			
Transmitter OFF Output Power	Poff			-30	dBm			
Transmitter Eye Mask		Compliant	with IEEE8	302.3ae				
	1	Receiver						
		1540	1550	1560		5483x		
Center Wavelength	λ	1480	1490	1500	nm	4583x		
Receiver Sensitivity (Average Power)	Sen.			-23	dBm	1		
Input Saturation Power (overload)	Psat	-8			dBm			
LOS Assert	LOSA	-35			dBm			
LOS De-assert	LOSD			-25	dBm			
LOS Hysteresis	LOSH	0.5			dB			

Notes:

1. Measured with Light source 1490nm @1550nm, ER=8.2dB; BER \leq 1E-12 @10.3125Gbps, PRBS= 2^{31} -1 NRZ.



VII. Digital Monitoring Functions

The following digital monitoring characteristics are defined over the Recommended Operating Environment unless otherwise specified. It is compliant to SFF-8472 Rev10.2 with internal calibration mode. For external calibration mode please contact our sales staff.

Parameter	Symbol	Min.	Max	Unit	Notes
Temperature monitor absolute error	DMI_ Temp	-3	3	°C	Over operating temp
Supply voltage monitor absolute error	DMI_VCC	-3%	3%	V	Full operating range
RX power monitor absolute error	DMI_RX	-3	3	dB	
Bias current monitor	DMI_ bias	-10%	10%	mA	
TX power monitor absolute error	DMI_TX	-3	3	dB	

VIII. Mechanical Dimensions

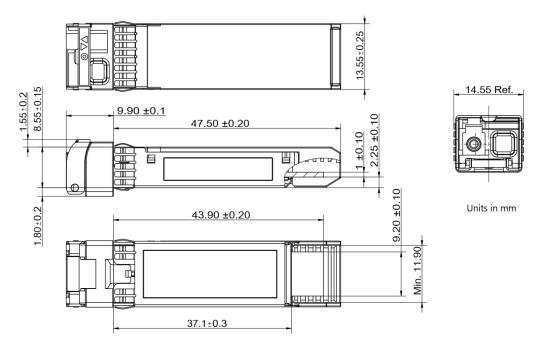


Figure 2. Mechanical Outline

IX. Revision History

Version No.	Initiated	Revised contents	Release Date
1.0	Andy Zhang	Preliminary datasheet	2014-06-11
1.1	Andy Zhang	Mechanical Change	2016-04-08



X. Contact us

Walsun Technology Co., Ltd

2-5# Tongfufu Industrial Zone, Aiqun Road, Shiyan Street, Baoan District, Shenzhen, China

T. +86 0755-23007456 F. +86 0755-23007451 PC. 518108

E. sales@walsun.com H. www.walsun.com