

100Gb/s QSFP28 CWDM4 2km Transceiver HX4X-CL3Q1C

Features

- 4 CWDM lanes Mux/De-mux design
- Up to 25.78Gbps Data rate per wavelength
- CWDM TOSA / ROSA for up to 2 km reach over SMF
- Duplex LC optical receptacle
- Support Digital Monitoring interface
- Build in CDR on both TX and RX
- RoHS-10 compliant and lead-free
- Compliant with QSFP28 MSA with LC connector
- Single +3.3V power supply
- Maximum power consumption 3.5W
- All-metal housing for superior EMI performance
- Case operating temperature
Commercial: 0 ~ +70°C



Applications

- Data Center
- 100G Ethernet & 100GBASE-LR4
- Ethernet switches and router applications

Part Number Ordering Information

Part Number	Data Rate (Gb/s)	Wavelength (nm)	Transmission Distance(km)	Temperature (°C) (Operating Case)
HX4X-CL3Q1C	100	1271、1291 1311、1331	2	0~70 commercial

I. Absolute Maximum Ratings

It has to be noted that the operation in excess of any individual absolute maximum ratings might cause permanent damage to this module.

Parameter	Symbol	Min	Max	Unit	Notes
Storage Temperature	T _S	-40	85	°C	
Power Supply Voltage	V _{CC}	-0.3	4.0	V	
Relative Humidity (non-condensation)	RH	5	95	%	
Damage Threshold	TH _d		5.0	dBm	

II. Recommended Operating Conditions

Parameter	Symbol	Min	Typical	Max	Unit	Notes
Operating Case Temperature	T _{OP}	0		70	°C	commercial
Power Supply Voltage	V _{CC}	3.135	3.3	3.465	V	
Data Rate			100		Gb/s	
Control Input Voltage High		2		V _{CC}	V	
Control Input Voltage Low		0		0.8	V	
Link Distance (SMF)	D			2	km	9/125um

III. General Description

Walsun'100G QSFP28 CWDM4 is designed to operate over single-mode fiber system using 4X25 CWDM channel in 1310 band and links up to 2km. The module converts 4 inputs channel of 25Gb/s electrical data to 4 CWDM optical signals, and multiplexes them into a single channel for 100Gb/s optical transmission. Reversely, on the receiver side, the module optically de-multiplexes a 100Gb/s input into 4 CWDM channels signals, and converts them to 4 channel output electrical data.

The central wavelengths of the 4 CWDM channels are 1271, 1291, 1311 and 1331 nm. It contains a duplex LC connector for the optical interface and a 38-pin connector for the electrical interface. Single-mode fiber (SMF) is applied in this module. This product converts the 4-channel 25Gb/s electrical input data into CWDM optical signals (light), by a 4-wavelength Distributed Feedback Laser (DFB) array. The 4 wavelengths are multiplexed into a single 100Gb/s data, propagating out of the transmitter module via the SMF. The receiver module accepts the 100Gb/s optical signals input, and de-multiplexes it into 4 CWDM 25Gb/s channels. Each wavelength light is collected by a discrete photo diode, and then outputted as electric data after amplified by a TIA. The product is designed with form factor, optical/electrical connection and digital diagnostic interface according to the QSFP28 Multi-Source Agreement (MSA) and compliant to IEEE 802.3bm.

IV. Pin Assignment and Pin Description

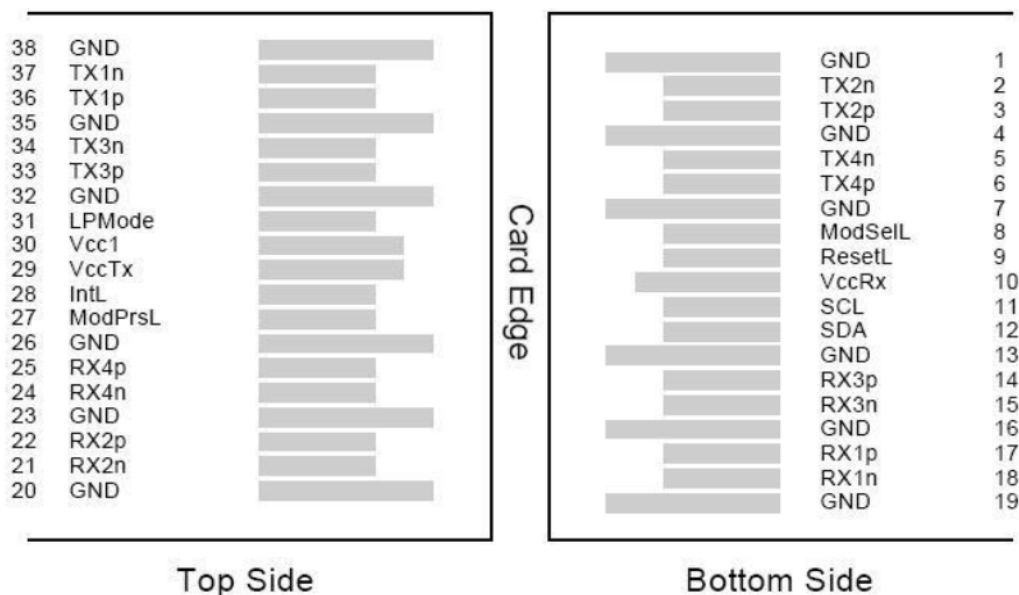


Figure1. Diagram of host board connector block pin numbers and names

Pin	Symbol	Name/Description	Notes
1	GND	Transmitter Ground (Common with Receiver Ground)	1
2	Tx2n	Transmitter Inverted Data Input	
3	Tx2p	Transmitter Non-Inverted Data output	
4	GND	Transmitter Ground (Common with Receiver Ground)	1
5	Tx4n	Transmitter Inverted Data Input	
6	Tx4p	Transmitter Non-Inverted Data output	
7	GND	Transmitter Ground (Common with Receiver Ground)	1
8	ModSelL	Module Select	
9	ResetL	Module Reset	
10	VccRx	3.3V Power Supply Receiver	2
11	SCL	2-Wire serial Interface Clock	
12	SDA	2-Wire serial Interface Data	
13	GND	Transmitter Ground (Common with Receiver Ground)	
14	Rx3p	Receiver Non-Inverted Data Output	
15	Rx3n	Receiver Inverted Data Output	

16	GND	Transmitter Ground (Common with Receiver Ground)	1
17	Rx1p	Receiver Non-Inverted Data Output	
18	Rx1n	Receiver Inverted Data Output	
19	GND	Transmitter Ground (Common with Receiver Ground)	1
20	GND	Transmitter Ground (Common with Receiver Ground)	1
21	Rx2n	Receiver Inverted Data Output	
22	Rx2p	Receiver Non-Inverted Data Output	
23	GND	Transmitter Ground (Common with Receiver Ground)	1
24	Rx4n	Receiver Inverted Data Output	1
25	Rx4p	Receiver Non-Inverted Data Output	
26	GND	Transmitter Ground (Common with Receiver Ground)	1
27	ModPrs1	Module Present	
28	IntL	Interrupt	
29	VccTx	3.3V power supply transmitter	2
30	Vcc1	3.3V power supply	2
31	LPMODE	Low Power Mode	
32	GND	Transmitter Ground (Common with Receiver Ground)	1
33	Tx3p	Transmitter Non-Inverted Data Input	
34	Tx3n	Transmitter Inverted Data Output	
35	GND	Transmitter Ground (Common with Receiver Ground)	1
36	Tx1p	Transmitter Non-Inverted Data Input	
37	Tx1n	Transmitter Inverted Data Output	
38	GND	Transmitter Ground (Common with Receiver Ground)	1

Notes:

1. GND is the symbol for signal and supply (power) common for QSFP28 modules. All are common within the QSFP28 module and all module voltages are referenced to this potential unless otherwise noted. Connect these directly to the host board signal common ground plane.
2. VccRx, Vcc1 and VccTx are the receiving and transmission power suppliers and shall be applied concurrently. Recommended host board power supply filtering is shown below. Vcc Rx, Vcc1 and Vcc Tx may be internally connected within the QSFP28 transceiver module in any combination. The connector pins are each rated for a maximum current of 500mA.

V. Electrical Characteristics

The following electrical characteristics are defined over the Recommended Operating Environment unless otherwise specified.

Parameter	Symbol	Min.	Typ.	Max	Unit	Notes
Power Consumption	p			3.5	W	
Supply Current	Icc			1060	mA	
Transmitter						
Single-ended Input Voltage Tolerance	Vcc	-0.3		4.0	V	
Differential Input Voltage Swing	Vin,pp	180		1000	mVp p	
Differential Input Impedance	Zin	90	100	110	Ohm	1
Transmit Disable Assert Time				10	us	
Transmit Disable Voltage	Vdis	Vcc-1.3		Vcc	V	
Transmit Enable Voltage	Ven	Vee		Vee +0.8	V	2
Receiver						
Differential Output Voltage Swing	Vout,pp	300		850	mVp p	
Differential Output Impedance	Zout	90	100	110	Ohm	3
Data output rise/fall time	Tr/Tf	28			ps	4
LOS Assert Voltage	VlosH	Vcc-1.3		Vcc	V	5
LOS De-assert Voltage	VlosL	Vee		Vee +0.8	V	5

Notes:

1. Connected directly to TX data input pins. AC coupled thereafter.
2. Or open circuit.
3. Input 100 ohms differential termination.
4. These are unfiltered 20-80% values.
5. Loss of Signal is LVTTTL. Logic 0 indicates normal operation; logic 1 indicates no signal detected.

VI. Optical Characteristics

The following optical characteristics are defined over the Recommended Operating Environment unless otherwise specified.

Parameter	Symbol	Min.	Typical	Max	Unit	Notes
Transmitter						
Lane wavelength (range)	L0	1264.5	1271	1277.5	nm	
	L1	1284.5	1291	1297.5	nm	
	L2	1304.5	1311	1317.5	nm	
	L3	1324.5	1331	1337.5	nm	
Signaling rate, each lane			25.78125		GBd	
Side-mode suppression ratio	SMSR	30				
Total launch power				8.5	dBm	
Average launch power, each lane	Pavg	-6.5		2.5	dBm	
Extinction Ratio	ER	3.5			dB	
Transmitter and Dispersion Penalty, each lane	TDP			3.3	dB	
OMA minus TDP, each lane	OMA-TDP	-2.3			dBm	
Average launch power of OFF transmitter, each lane				-30	dBm	
Transmitter reflectance				-12	dB	
Transmitter eye mask {X1, X2,X3, Y1, Y2, Y3}		{0.25, 0.4, 0.45, 0.25, 0.28, 0.4}				
Receiver						
Signaling rate, each lane			25.78125		GBd	
Receiver Sensitivity per lane	Rsen			-10	dBm	1
Input Saturation Power (overload)	Psat			2.5	dBm	
LOS Assert	LOSA	-30			dBm	
LOS De-assert	LOSD			-12		

Receiver reflectance	Rr			-26		
LOS Hysteresis		0.5		4	dB	

Notes:

1. Measured with a PRBS 2³¹ -1 test pattern, @25.78Gb/s, BER<1E-12.

VII. Digital Diagnostic Functions

The following digital diagnostic characteristics are defined over the normal operating conditions unless otherwise specified.

Parameter	Symbol	Min.	Max	Unit	Notes
Temperature monitor absolute error	DMI_Temp	-3	3	°C	Over operating temp
Supply voltage monitor absolute error	DMI_VCC	-0.15	0.15	V	Full operating range
RX power monitor absolute error	DMI_RX	-3	3	dB	
Bias current monitor	DMI_bias	-10%	10%	mA	
TX power monitor absolute error	DMI_TX	-3	3	dB	

VIII. Mechanical Dimensions

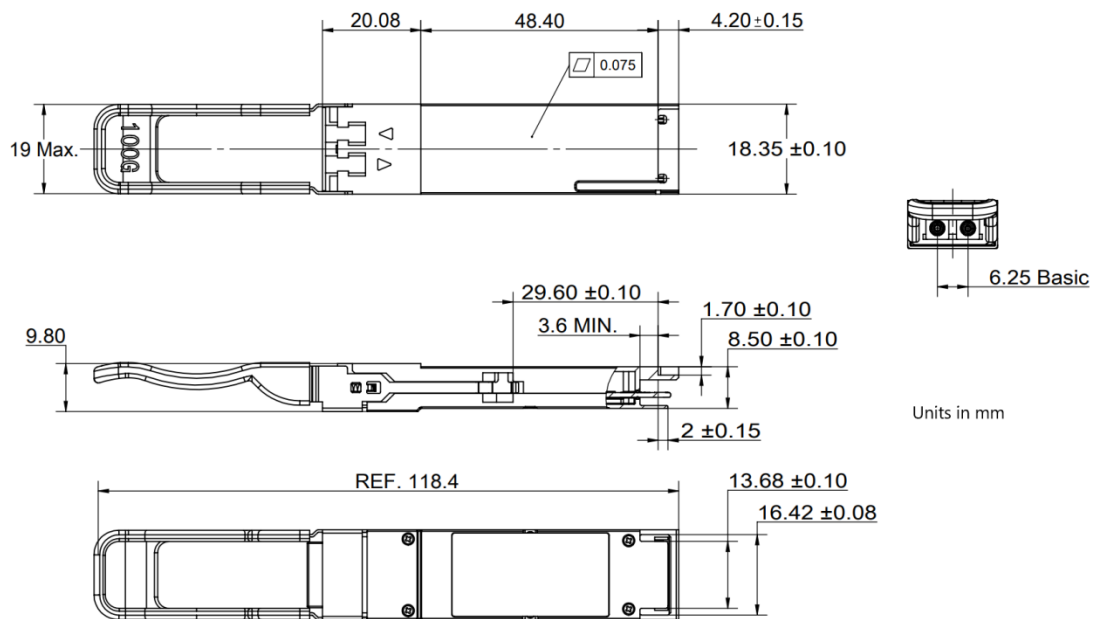


Figure2. Mechanical Outline

IX. Revision History

Version No.	Initiated	Revised contents	Release Date
V1.0	Andy Zhang	Preliminary datasheet	2018-09-20

X. Contact us

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